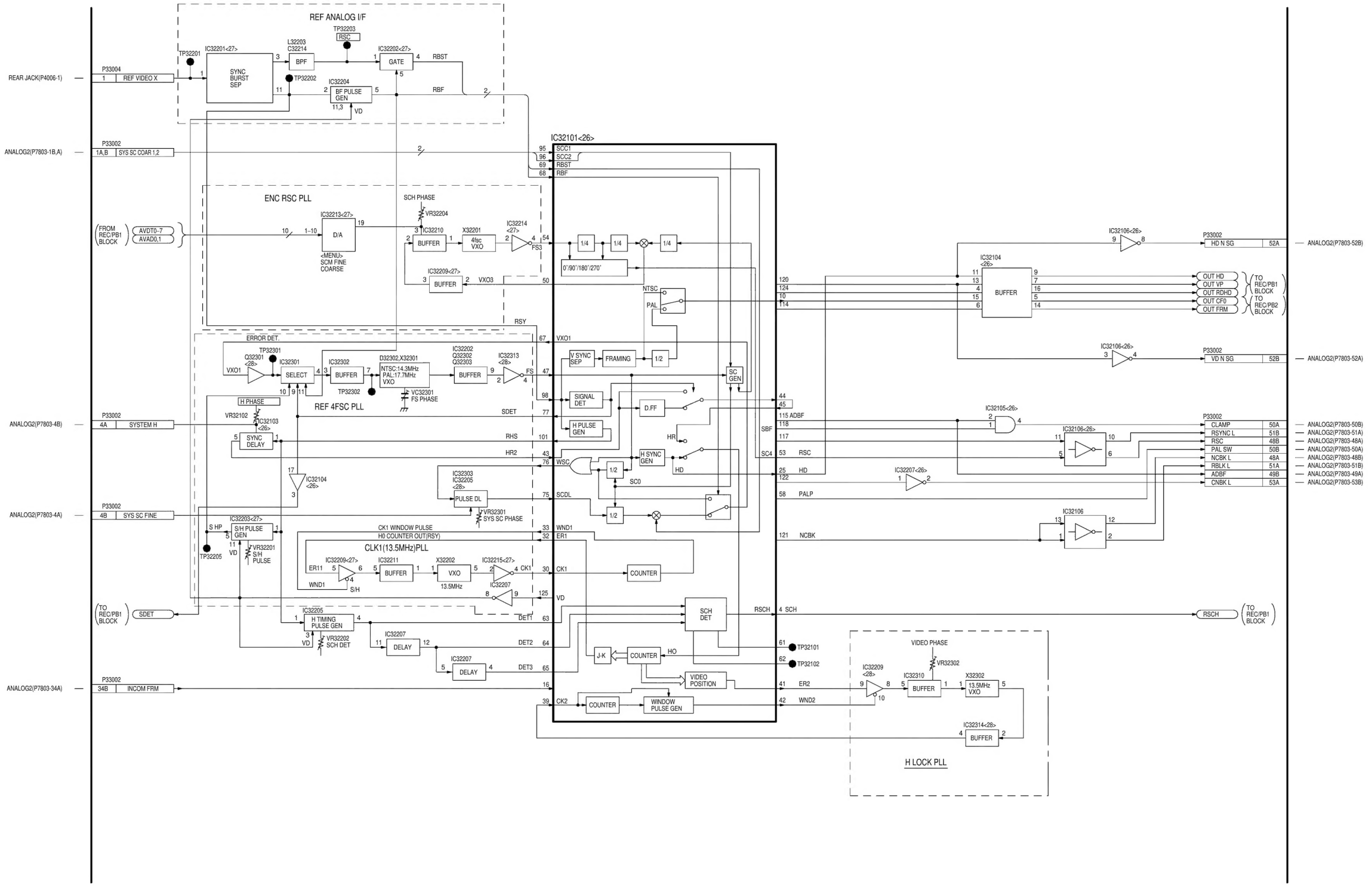
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TP32203
RSC
TP32201
IC32201<27>
SYNC BURST SEP
L32203
C32214
BPF
IC32202<27>
GATE
RBST
TP32202
IC32204
BF PULSE GEN
RBF
VD
REAR JACK(P4006-1)
P33004
1
REF VIDEO X
ANALOG2(P7803-1B,A)
P33002
1A,B
SYS SC COAR 1,2
IC32101<26>
SCC1
SCC2
RBST
RBF
ENC RSC PLL
SCH PHASE
VR32204
IC32213<27>
D/A
<MENU> SCM FINE COARSE
FROM REC/PB1 BLOCK
AVDT0–7
AVAD0,1
IC32210
BUFFER
X32201
4fsc VXO
IC32214 <27>
FS3
IC32209<27>
VXO3
1/4
0°/90°/180°/270°
NTSC
PAL
RSY
ERROR DET.
TP32301
Q32301 <28>
IC32301
SELECT
IC32302
BUFFER
D32302,X32301
NTSC:14.3MHz
PAL:17.7MHz
VXO
TP32302
VC32301
FS PHASE
IC32202
Q32302
Q32303
IC32313 <28>
FS
VXO1
V SYNC SEP
FRAMING
1/2
SC GEN
H PHASE
VR32102
IC32103 <26>
REF 4FSC PLL
SDET
SIGNAL DET
D.FF
H PULSE GEN
ANALOG2(P7803-4B)
P33002
4A
SYSTEM H
SYNC DELAY
RHS
HR
HR2
H SYNC GEN
HD
WSC
SC0
IC32104 <26>
IC32303
IC32205 <28>
PULSE DL
SCDL
VR32301
SYS SC PHASE
ANALOG2(P7803-4A)
P33002
4B
SYS SC FINE
S HP
IC32203<27>
S/H PULSE GEN
CK1 WINDOW PULSE
H0 COUNTER OUT(RSY)
WND1
ER1
CLK1(13.5MHz)PLL
IC32209<27>
IC32211
IC32202
IC32215<27>
ER11
WND1
S/H
BUFFER
VXO
13.5MHz
CK1
COUNTER
TP32205
VR32201
S/H PULSE
IC32207
VD
TO REC/PB1 BLOCK
SDET
IC32205
H TIMING PULSE GEN
VR32202
SCH DET
DET1
DET2
DET3
IC32207
DELAY
SCH DET
RSCH
J-K
HO
VIDEO POSITION
WINDOW PULSE GEN
CK2
ANALOG2(P7803-34A)
P33002
34B
INCOM FRM
TP32101
TP32102
ER2
WND2
VIDEO PHASE
VR32302
IC32209 <28>
IC32310
X32302
13.5MHz VXO
IC32314<26>
H LOCK PLL
IC32106<26>
IC32104 <26>
OUT HD
OUT VP
OUT RDHD
OUT CF0
OUT FRM
TO REC/PB1 BLOCK
TO REC/PB2 BLOCK
HD N SG
VD N SG
IC32105<26>
IC32106<26>
IC32207<26>
IC32106
CLAMP
RSYNC L
RSC
PAL SW
NCBK L
RBLK L
ADBF
CNBK L
ANALOG2(P7803-52B)
ANALOG2(P7803-52A)
ANALOG2(P7803-50B)
ANALOG2(P7803-51A)
ANALOG2(P7803-48A)
ANALOG2(P7803-50A)
ANALOG2(P7803-48B)
ANALOG2(P7803-51B)
ANALOG2(P7803-49A)
ANALOG2(P7803-53B)